



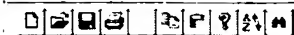
DBs:

Default operator:

☒ Plurals
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12 and "bias voltage"

[⚡ Hds](#)
[❤️ Details](#)
[🇺🇸 HTML](#)



- Drafts

- Pending

- Active

- L1: (67648) "455"/\$.ccls.
- L2: (1989) 1 and ((multiple or plurality or plural or dual) near8 "frequency band")
- L3: (1110) 2 and amplifier
- L4: (81) 3 and ("high pass filter" and "low pass filter")
- L5: (11) 4 and ("high frequency band" and "low frequency band")
- L6: (7) 4 and ("high frequency band" and "low frequency band")
- L7: (0) 6 and antenna
- L8: (4673) antenna and ((multiple or plurality or plural or dual) near8 "frequency band")
- L9: (2258) 8 and amplifier
- L10: (185) 9 and ("high pass filter" and "low pass filter")
- L11: (2) 10 and ("high frequency band" and "low frequency band")
- L12: (2) 11 and transistor
- L14: (1) 12 and "bias voltage"
- L13: (2) 12 and voltage

- Failed

- Saved

- Favorites

- Tagged (2)

- UDC

- Queue

- Trash

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DBs US-PGPUB; USPAT; EPO

☒ Plurals

Default operator OR

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12 and voltage








BRS form IS&R form Image Text HTML


	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current X	Retr	Inventor	S
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6054902 A	20000425	26	High-frequency amplifier	330/306	330/147; 330/148		Masato; Yoshinobu	<input checked="" type="checkbox"/>
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5926466 A	19990720	58	Tune division multiple access FDD wireless unit and tune division multipl	370/280	370/281; 370/347		Ishida; Kaoru et al.	<input checked="" type="checkbox"/>

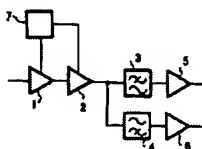
Hits Details HTML

Ready

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-  Failed
-  Saved
-  Favorites
-  Tagged (2)
-  UDC
-  Queue
-  Trash

<div style="text-align: center;">  0123456789 </div>	
to United States Patent Application Publication	
(2) Pub. No. US 2002/0132437 A1 (3) Pub. Date: Sep. 19, 2002	(1) Pub. No. _____ (2) Pub. Date: _____
(10) TRANSMITTING CIRCUIT AND COMMUNICATION TERMINAL DEVICE THEREOF	
(11) Inventor: Katsuhiko Itoh, Tokyo, JP	(12) ABSTRACT
Correspondence Address: ROBERTS WERN PULF 2200 PLYMOUTH HILLWAY, S.W. WASHINGTON, DC 20007 USA	
(7) Attorney: KGC CORPORATION	(13) ACLA No.: 00000000
(14) Filed: Mar. 6, 2002	(15) Publication Classification
(16) Foreign Application Priority Data	(17) Pub. No.: 2001-075000
(18) Int. Cl. 7: H04L 1/00	(19) Int. Cl. 8: H04L 1/00



1: FIRST STAGE AMPLIFIER

2: SECOND STAGE AMPLIFIER

3: LOW-PASS FILTER

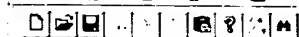
4: HIGH-PASS FILTER

5: GSM 900 FREQUENCY BAND AMPLIFIER

6: GSM 1800 FREQUENCY BAND AMPLIFIER

7: BIAS VOLTAGE SETTING CIRCUIT

	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current X	Retr	Inventor	S
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20020132637 A1	20020919	5	Transmitting circuit and communication terminal unit therewith	455/553.1	455/188.1		Ishii, Katsuhiko	<input checked="" type="checkbox"/>



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 - ☒ L14: (1) 12 and "bias voltage"
 - ☒ L13: (2) 12 and voltage
 - ☒ L15: (155) "DCS 1800" and "GSM 900"
 - ☒ L16: (1) 15 and "class C amplifier"
 - ☒ L17: (4) 15 and "GaAs"
- ☐ Failed
- ☐ Saved
- ☐ Favorites
- ☐ Tagged (3)
- ☐ UDC
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DBs: US-PGPUB; USPAT, EPO

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15 and "GaAs"

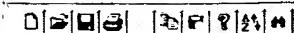
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1	<input type="checkbox"/>	<input type="checkbox"/>	US 20020132637 A1	20020919	5	Transmitting circuit and communication terminal unit therewith	455/553.1	455/188.1		Ishii, Katsuhiko	<input checked="" type="checkbox"/>
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6815707 B2	20041109	16	Field-effect type semiconductor device for power amplifier	257/19	257/18; 257/192		Sugii, Nobuyuki et al.	<input checked="" type="checkbox"/>
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6392610 B1	20020521	20	Antenna device for transmitting and/or receiving RF waves	343/876	343/700MS; 343/702		Braum, Christian et al.	<input checked="" type="checkbox"/>
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6297694 B1	20011002	18	High frequency power amplifier	330/51	330/134; 330/295		Yamamoto, Kazuya	<input checked="" type="checkbox"/>

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- L17: (4) 15 and "GaAs"
- L18: (1) 17 and "class C amplifier"
- L19: (4) 17 and amplifier
- L20: (3) 19 and "bias voltage"
- L23: (0) 21 and ("high frequency band" and "low frequency band")
- L22: (1) 21 and ("high pass filter" and "low pass filter")
- L21: (3) 20 and transistor

Failed

Saved

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DBs US-PGPUB; USPAT; EPO

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20 and transistor

BRS form IS&R form Image Text HTML

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